

each pixel region being formed by adjacent image signal lines and adjacent scan signal lines having at least an active device;

at least a pixel electrode connected to the active device and at least a counter electrode in each pixel, the pixel electrode and the counter electrode are on the first substrate;

a first alignment film formed over the pixel electrode and counter electrode on the first substrate at least in the pixel forming region;

B1
Amended a second alignment film formed on the second substrate at least in the pixel forming region;

wherein rubbing directions of first and second alignment films are substantially parallel to each other; and

wherein the pixel electrode and the counter electrode are disposed on a same insulating layer which is arranged under the first alignment film and which is arranged over at least one of the image signal lines. *fig 26*

Please amend claim 6 to read as follows.

6. (Amended) An active matrix liquid crystal display device according to claim 1, wherein at least one of the pixel electrode and the counter electrode is a transparent electrode.

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B3 Please amend claim 8 to read as follows.

8. (Amended) An active matrix liquid crystal display device according to claim 6, wherein the pixel electrode and the counter electrode are transparent electrodes.

Please add the following new claims to the application.

-9. An active matrix liquid crystal display device comprising first and second substrates;

a liquid crystal layer disposed between the first and second substrates;

plural image signal lines and scan signal lines formed on the first substrate, and

each pixel region being formed by adjacent image signal lines and adjacent scan signal lines having at least an active device;

at least a pixel electrode connected to the active device and at least a counter electrode in each pixel, the pixel electrode and the counter electrode are on the first substrate;

a first alignment film formed over the pixel electrode and counter electrode on the first substrate at least in the pixel forming region;

a second alignment film formed on the second substrate at least in the pixel forming region,

wherein the pixel electrode and the counter electrode are disposed on a same insulating layer which is arranged under the first alignment film and which is arranged over at least one of the image signal lines.

10. An active matrix liquid crystal display device according to claim 9, wherein the counter electrode is a transparent electrode.

11. An active matrix liquid crystal display device according to claim 10, wherein the pixel electrode is a transparent electrode.

12. An active matrix liquid crystal display device according to claim 9, further comprising a source electrode connected to the active device, and the source electrode

and the pixel electrode are connected to each other at a thorough hole.

13. An active matrix liquid crystal display device according to claim 12, further comprising a counter signal line in each pixel, and the thorough hole being arranged at a region where the counter signal line is formed.

14. An active matrix liquid crystal display device according to claim 13, wherein the source electrode has wider portion on and closer to the counter signal line than to the active device, and the wider portion forms a storage capacitor with the counter signal line.

15. An active matrix liquid crystal display device comprising first and second substrates;

a liquid crystal layer disposed between the first and second substrates;

plural image signal lines and scan signal lines formed on the first substrate, and each pixel region being formed by adjacent image signal lines and adjacent scan signal lines having at least an active device;

at least a pixel electrode connected to the active device and at least a counter electrode in each pixel, the pixel electrode and the counter electrode are on the first substrate;

wherein a pair of counter electrodes is provided and arranged with one of the image signal lines between the counter electrodes of the pair, and a member is provided on the first substrate so as to shield light in a region between the pair of counter electrodes.

16. An active matrix liquid crystal display device according to claim 15, wherein the member is arranged at a different layer than a layer of the one of the image signal lines

and a layer of the pair of counter electrodes.

17. An active matrix liquid crystal display device according to claim 16, wherein the layer of the member is arranged below the layer of the one of the image signal lines.

18. An active matrix liquid crystal display device according to claim 17, wherein the member is arranged at a gate layer.

19. An active matrix liquid crystal display device according to claim 15, wherein the member is an electrode.

20. An active matrix liquid crystal display device comprising;
first and second substrates;
a liquid crystal layer disposed between the first and second substrates;
plural image signal lines and scan signal lines formed on the first substrate, and
each pixel region being formed by adjacent image signal lines and adjacent scan signal lines having at least an active device;

at least a pixel electrode connected to the active device and at least a counter electrode in each pixel, the pixel electrode and the counter electrode are on the first substrate;

wherein a pair of counter electrodes is provided and arranged with one of the image signal lines between the counter electrodes of the pair,

and a member is provided on the first substrate and which is elongated in a same direction as a direction of the one of the image signal lines, and the member has an overlapping relation with the one of the image signal lines and the pair of counter electrodes.

21. An active matrix liquid crystal display device according to claim 20, wherein the

member is arranged at a different layer than a layer of the one of the image signal lines and a layer of the pair of the counter electrodes.

22. An active matrix liquid crystal display device according to claim 21, wherein the layer of the member is arranged below the layer of the one of the image signal lines.

23. An active matrix liquid crystal display device according to claim 22, wherein the member is arranged at a gate layer.

24. An active matrix liquid crystal display device according to claim 20, wherein the member is an electrode.

25. An active matrix liquid crystal display device comprising:

first and second substrates;

a liquid crystal layer disposed between the first and second substrates;

plural image signal lines and scan signal lines formed on the first substrate, and each pixel region being formed by adjacent image signal lines and adjacent scan signal lines having at least an active device;

at least a pixel electrode connected to the active device and at least a counter electrode in each pixel, the pixel electrode and the counter electrode are on the first substrate; and

shielding means being provided for shielding light in a region between the counter electrode and one of the image signal lines at the first substrate.
